

Substitute for Form 1449/PTO				Complete if Known		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	09/954,777	
				Filing Date	September 17, 2001	
				First Named Inventor:	Mark Greenberg	
				Art Unit	2631	
				Examiner Name	Not Yet Assigned	
Sheet	1	of	2	Attorney Docket Number	004259.P034	
U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
JAM		US-	4,015,238	3/29/1977	Robert Curtis Davis	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
JAM		WO	00/74264	A1	12/7/2000	Martone, Massimiliano		
JAM		WO	01/59937	A2	8/16/2001	Qualcomm Inc.		

Examiner Signature	Jarol Muel	Date Considered	2/3/05
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NON PATENT LITERATURE DOCUMENTS			
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		E. VERRIEST, ISEN, "Implementing an Adaptive Noise Cancelling System to Enhance Sonar Receiver Performance Using the TMS320C31 DSP," ESIEE, Paris, September 1996, Texas Instruments, Pages 1-24.	
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		BREE, ET AL., "A Bit-Serial Architecture For A VLSI Viterbi Processor", Communications Systems Research Group, University of Saskatchewan, Saskatoon, IEEE, WESCANEX '88, 1988, pages 72-77.	
		BIVER, ET AL., "Architectural Design and Realization Of A Single-Chip Viterbi Decoder", Elsevier Science Publishers B.V., INTEGRATION, The VLSI Journal 8 (1989), October, No. 1, Amsterdam, NL, Pages 3-16.	
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		MARIE-LAURE BOUCHERET, IVAR MORTENSEN and HENRI FAVARO, "Fast Convolution Filter Banks For Satellite Payloads with On-Board Processing," IEEE Journal On Selected Areas In Communications, Vol. 17, No. 2, February 1999	
		W.H. YIM and F.P. COAKLEY, "On-Board Processing For KA-Band Applications", University of Surrey, UK, Publication Date, February 11, 1993., XP 000458011, pp. 225-229.	
	HASHIDA MITSUYOSHI, "Hierarchical Network Management System and Control Method for Network Management Information," Patent Abstracts of Japan, Publication No. 07226777		

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PTO/SB/08A (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

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Application Number	09/954,777
Filing Date	September 17, 2001
First Named Inventor	Mark Greenberg
Art Unit	2631
Examiner Name	Unassigned
Attorney Docket Number	4259P034

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.†	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code* (if known)			
JAM		US-6,157,997	12-05-2000	Oowaki, et al.	
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OTHER ART - NON PATENT LITERATURE DOCUMENTS

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Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)	Attorney Docket No.: 04259.P034	Application Number: 09/954,777
	First Named Inventor: Mark Greenberg, et al.	
	Filing Date: September 17, 2001	



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Exam. Initial*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ³ (If known)			
		6,148,043		Fujimoto	11/14/2000	
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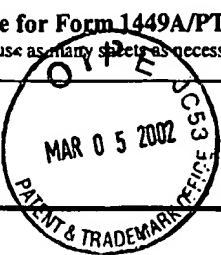
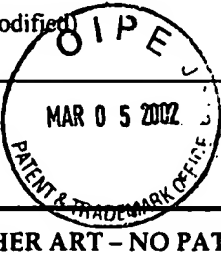
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OTHER ART - NO PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²
JGM		JOHN G. PROAKIS, Block and Convolutional Channel Codes, Digital Communications, Fourth Edition, Pages 416 - 547, McGraw-Hill Series in Electrical and Computer Engineering, New York, NY.	
		"www.inventra.com/inventra/softcore/workshop/MultiRaFiltDes95/" MENTOR GRAPHICS, Hardware Design of Decimators/Interpolators, Pages 1-38.	
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		http://www.mentor.com/inventra/softcore/workshop/Applications95/, MENTOR GRAPHICS, Sigma Delta Converter Applications, Pages 1-5.	

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